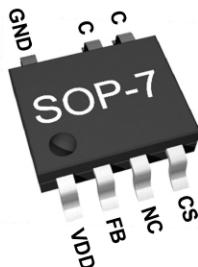




DP2701X

High Performance Primary Side Regulation CV/CC Power Switch

Pin Configuration



SOP-7

Marking Information



SOP-7

DP2701X for product name

XXXXXX The first X represents the revision level (0-4), the second X represents the revision date in A-L, the third and fourth A are digits of the date, the fifth, the last two digits represent the wafer location (00-22).

Typical Output Power Table

DP27A

DP27B

DP27C

DP27D

DP27E

DP27F

Note 1: The Max QSSA of the device is determined by the maximum output power.

Note 2: 2%GAC or 100mVAC voltage drop is used.

Note 3: Typical conduction losses from VDD to VFB at 100mA current with 100°C ambient temperature.

